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RESPONSE UNDER 37 C.F.R. 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2183

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Hoyle et al. (TI-30561)

Serial No. 09/702,405

Filed: October 31, 2000

For: Microprocessor for Having a Set of Byte Interspersing Instructions

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AMENDMENT UNDER RULE 116

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Introductory Comments:

This paper is presented in response to the Office Action mailed on May 28, 2004, in which claims in this case were finally rejected. It is submitted that, upon entry of this amendment, all claims will be in condition for allowance, or alternatively that the issues will be in better condition for appeal. Entry of this amendment in, and reconsideration of, this application are respectfully requested.

Proposed amendment to the specification begins on page 2.

Proposed amendment to the claims begins on page 3.

Applicants' remarks begin on page 8.

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